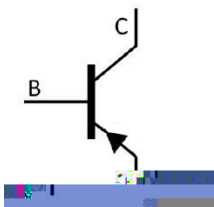


/ Descriptions

KF \$0) GE G Silicon PNP transistor in a TO-92 Plastic Package.

) J: () (* Žž
Complementary pair with 2SC1213(A).

Low frequency power amplifier.



PIN1 Base PIN 2 Collector PIN 3 Emitter

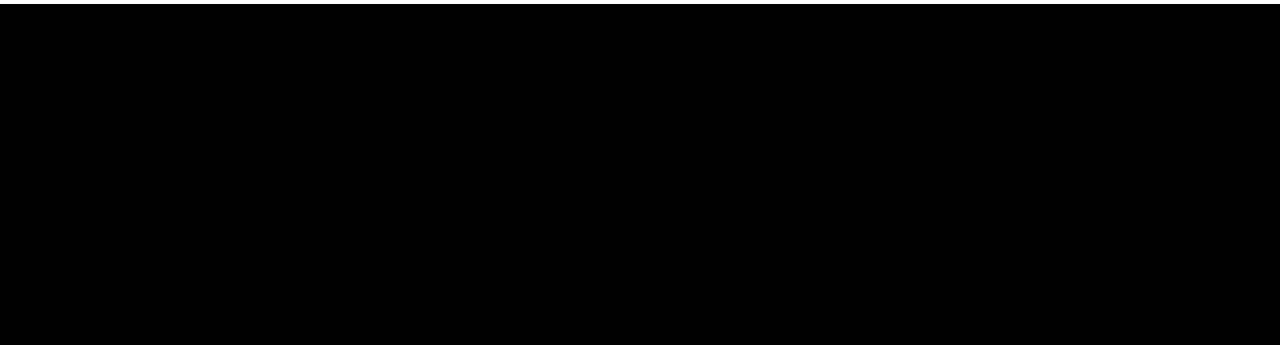
/ hFE Classifications & Marking

/ Absolute Maximum Ratings(Ta=25)

Parameter	Symbol	Rating	Unit	
Collector to Base Voltage	V _{CBO}	2SA673	-35	V
		2SA673A	-50	
Collector to Emitter Voltage	V _{CEO}	2SA673	-35	V
		2SA673A	-50	
Emitter to Base Voltage	V _{EBO}	-4.0	V	
Collector Current - Continuous	I _C	-500	mA	
Collector Power Dissipation	P _C	400	mW	
Junction Temperature	T _j	150		
Storage Temperature Range	T _{stg}	-55 150		

/ Electrical Characteristics(Ta=25)

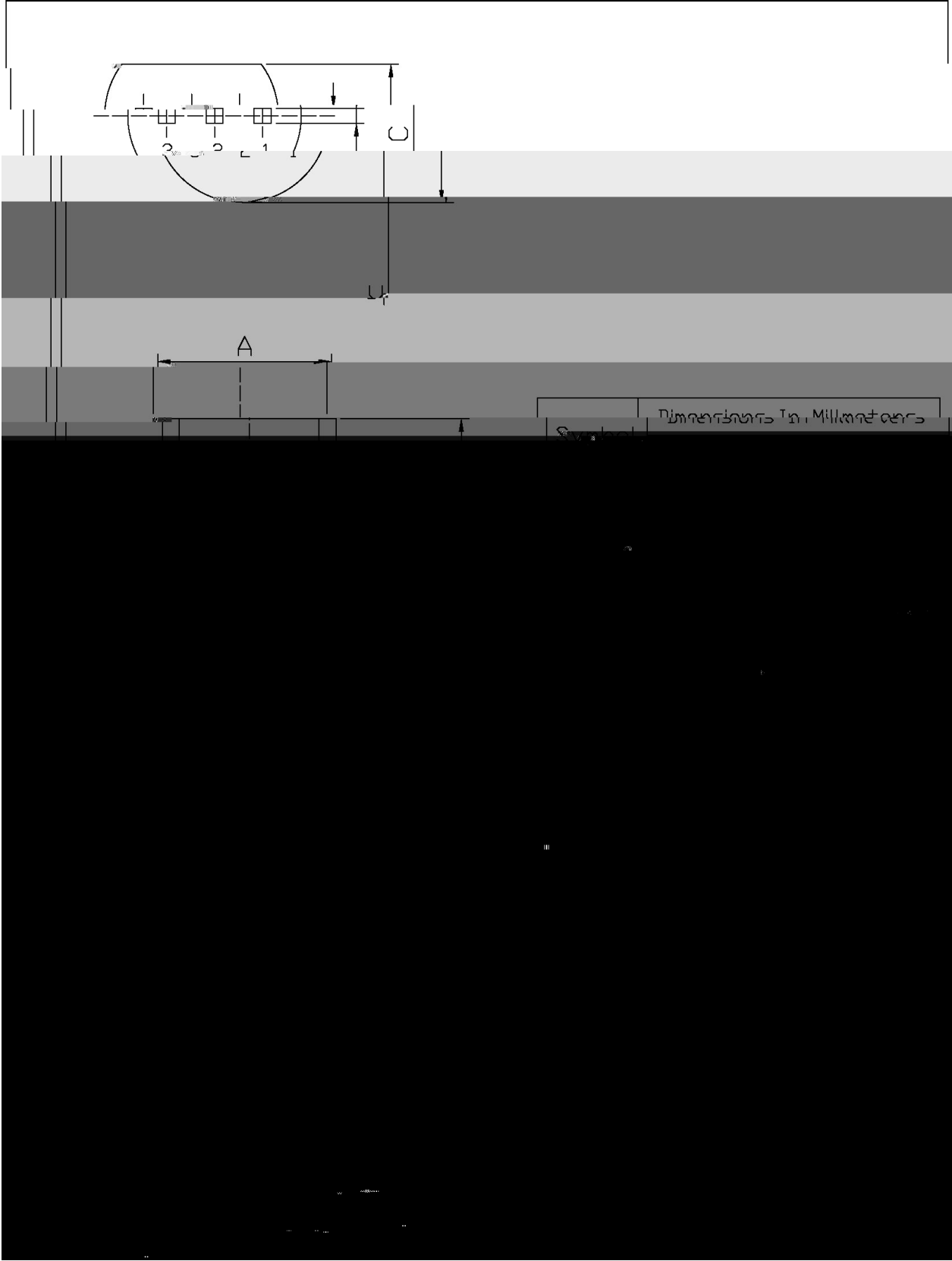
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector to Base Breakdown Voltage	V _{CBO}	I _C =-10 A I _E =0	-35			V
			-50			
Collector to Emitter Breakdown Voltage	V _{CEO}	I _C =-1.0mA I _B =0	-35			V
			-50			
Emitter to Base Breakdown Voltage	V _{EBO}	I _E =-10 A I _C =0	-4.0			V
Collector Cut-Off Current	I _{CBO}	V _{CB} =-20V I _E =0			-0.5	A
DC Current Gain	h _{FE(1)}	V _{CE} =-3.0V I _C =-10mA	60		320	
	h _{FE(2)}	V _{CE} =-3.0V I _C =-500mA	10			
Collector to Emitter Saturation Voltage	V _{CE(sat)}	I _C =-150mA I _B =-15mA		-0.2	-0.6	V
Base to Emitter Voltage	V _{BE}	V _{CE} =-3.0V I _C =-10mA		-0.64		V



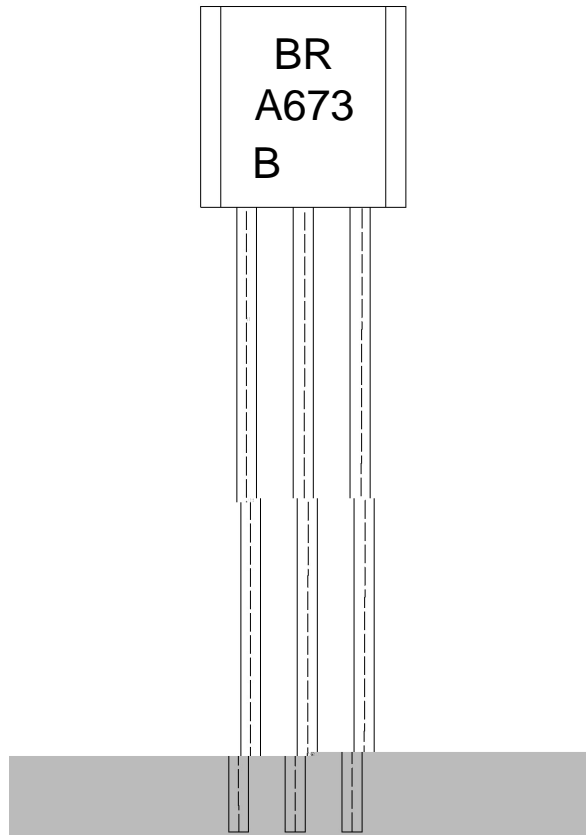
/ Package Dimensions

T0-92

Unit: mm



/ Marking Instructions



91 1
8- . *
91 h_{FE}

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Note:

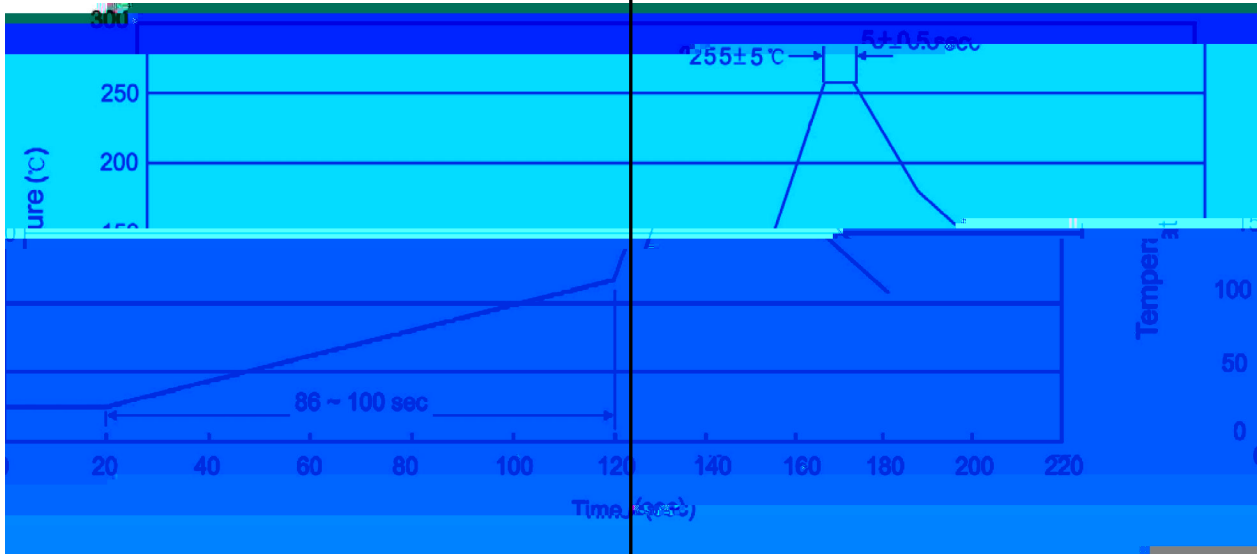
BR: Company Code.

A673: Product Type.

B: h_{FE} Classifications Symbol

****: Lot No. Code,code change with Lot No.

() / Temperature Profile for Dip Soldering(Pb-Free)



- 1 25 150 60 90sec;
- 2 255..5 5..0.5sec;
- 3 2 10 /sec.

Note:

- 1.Preheating:25~150 , Time:60~90sec.
- 2.Peak Temp.:255..5 , Duration:5..0.5sec.
- 3. Cooling Speed: 2~10 /sec.

/ Resistance to Soldering Heat Test Conditions

270..5 10..1 sec.

Temp:270±5 Time:10±1 sec

/ Packaging SPEC.

/ BULK

Package Type 封装形式	Units 包装数量				Dimension 包装尺寸 (unit: mm3)			
	只袋	袋盒	只盒	盒箱	只箱	袋	盒	箱