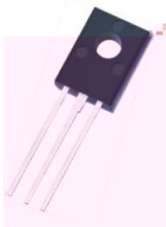
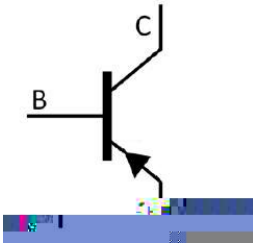


/ Descriptions

TO-126F(R) PNP Silicon PNP transistor in a TO-126F(R) Plastic Package.

Low $V_{CE(sat)}$, High V_{CEO} .

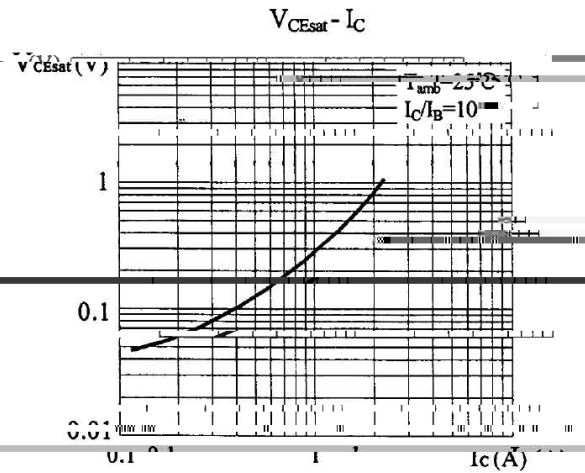
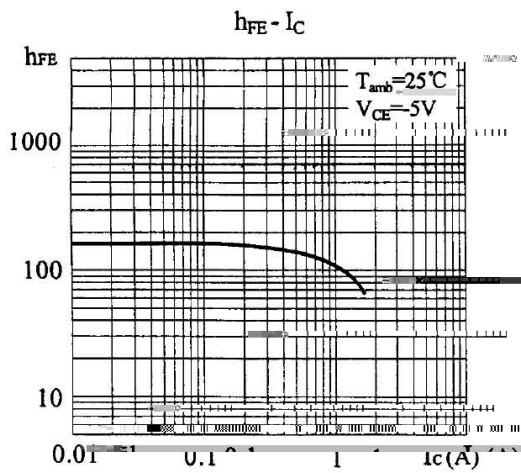
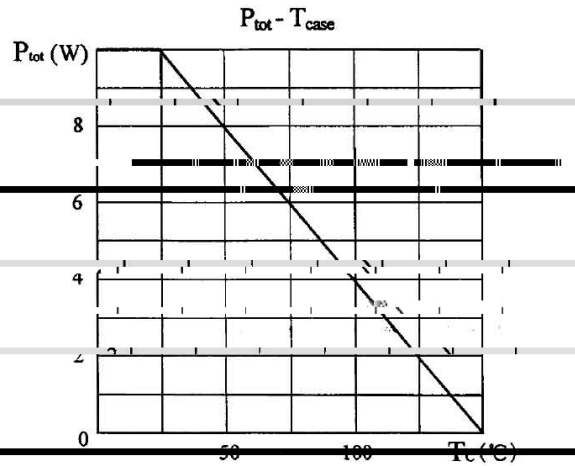
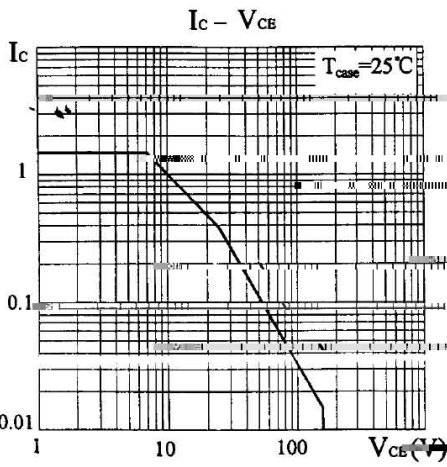
Low frequency power amplifier applications.



PIN1 Base PIN 2 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

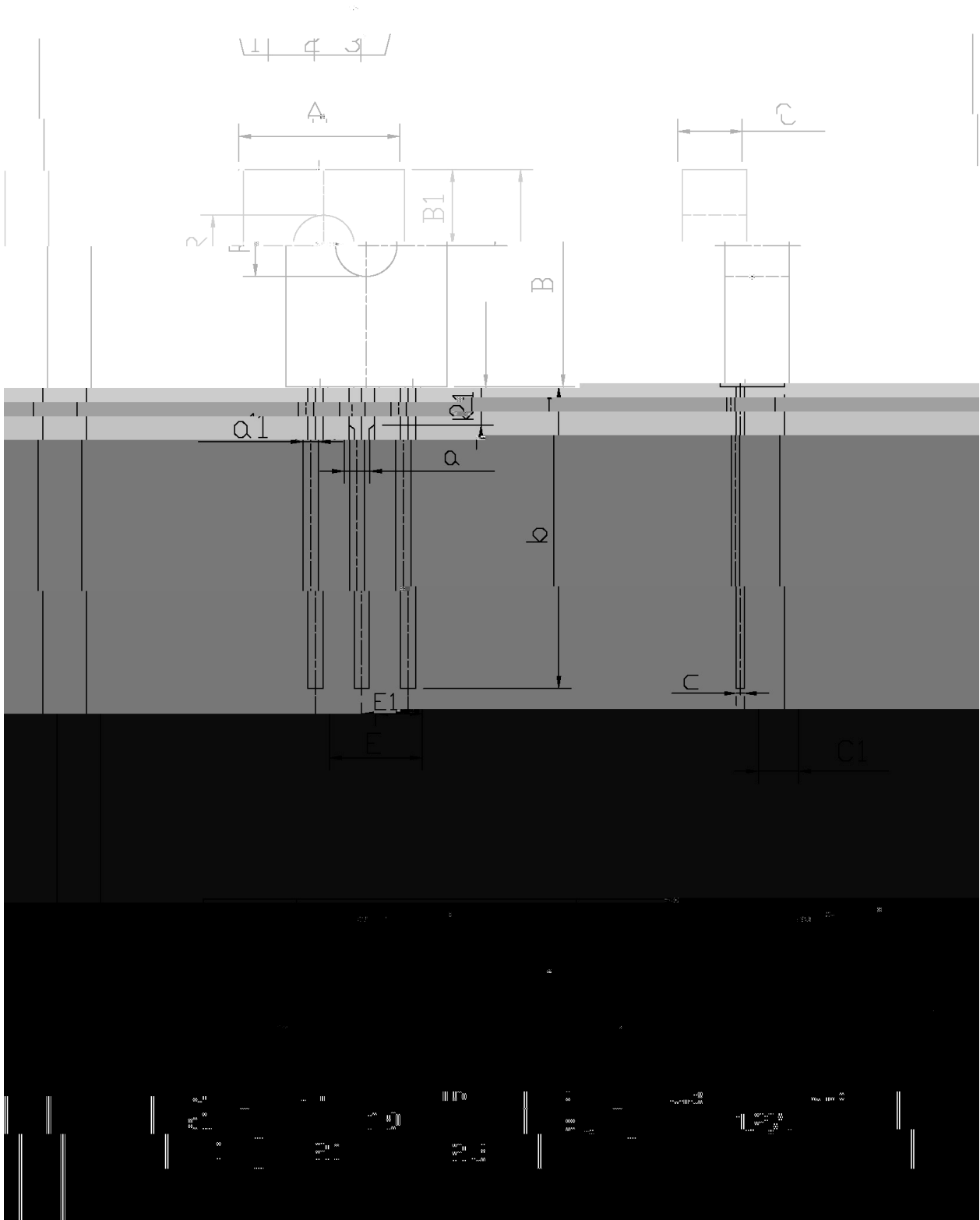
/ Electrical Characteristic Curve



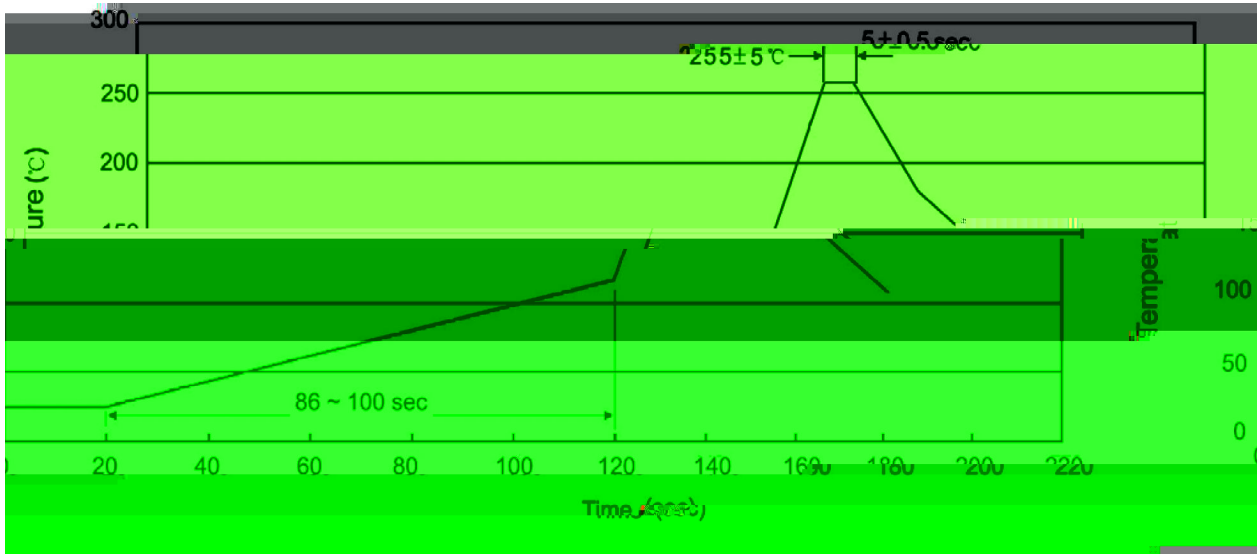
/ Package Dimensions

TOP VIEW

FRONT VIEW



() / Temperature Profile for Dip Soldering(Pb-Free)



Note:

- | | | | | | |
|---|-------|-----|-------------|---|--------------------------------------|
| 1 | 25 | 150 | 60 | 90sec; | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 255±5 | | 5±0.5sec; | 2.Peak Temp.:255±5 , Duration:5±0.5sec. | |
| 3 | | | me:60~909b/ | | |