

TO\$))'

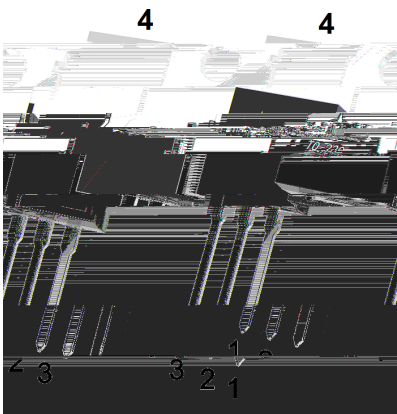
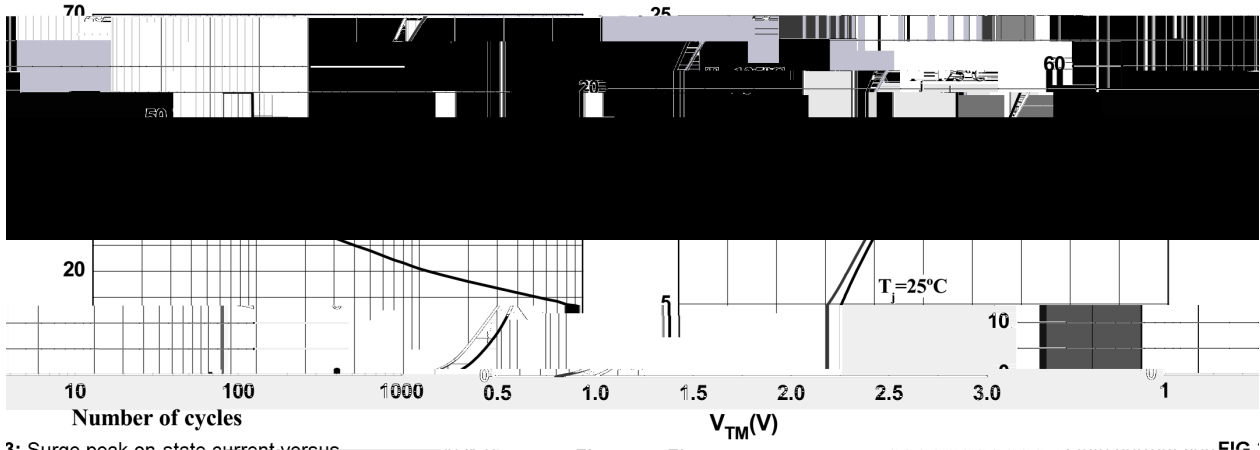




FIG 1 Maximum power dissipation

Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature



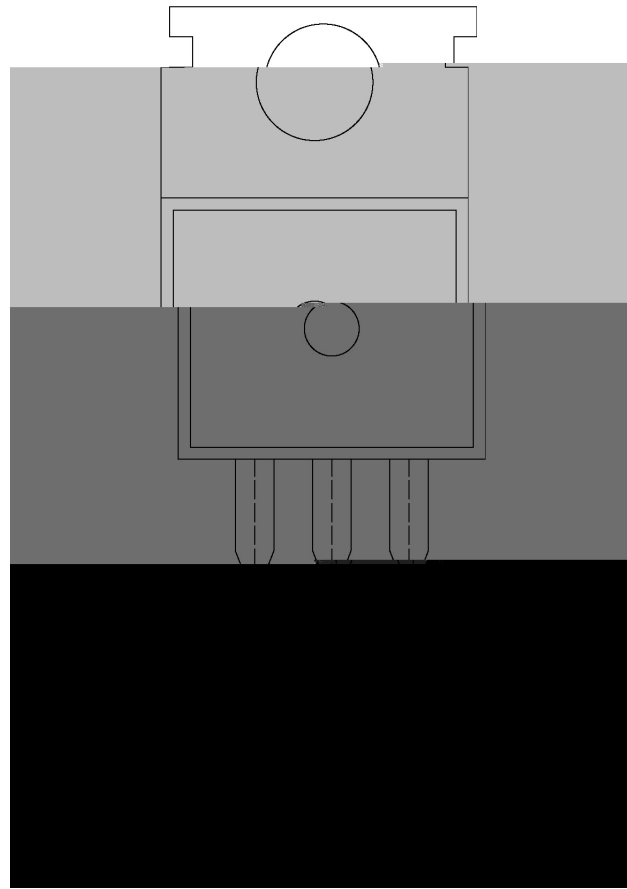
3: Surge peak on state current versus

FIG 3



FIG.5: Non-repetitive surge peak on-state current

FIG 6: Relative variations of gate trigger

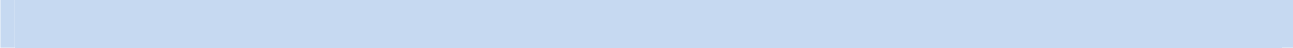


9R

E

I_{GT}

9T' 8' 8



±

±

±

±