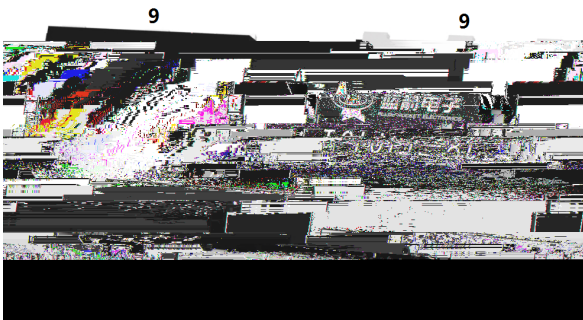
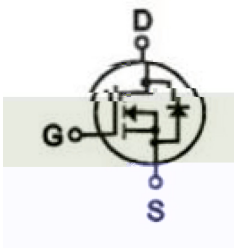


Rev.A Jul.-2025

TOLL-8L N  
N-Channel MOSFET in a TOLL-8L Plastic Package.

$V_{DS}(V)=100V$      $I_D=300A$   
 $R_{DS(ON)}@10V$  1.3m (Typ. 1.1m )  
 $R_{DS(ON)}@6V$  2.0m (Typ. 1.5m )  
 \* HF Product.

LCDM  
 LCD TV appliances,LCDM appliances,High power inverter system.



PIN1 G    PIN2 3 4 5 6 7 8 S    PIN9 D

See Marking Instructions.

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Drain Current(DC)***	$I_D(T_C=25 \text{ } )$	300	A
	$I_D(T_C=100 \text{ } )$	215	A
Drain Current – Pulsed*,**	$I_{DM}$	1200	A
Gate-Source Voltage	$V_{GS}$	w 20	V
Power Dissipation	$P_{tot}$	250	W
Continuous-Source Current	$I_S$	400	A
Single Pulse Avalanche Energy( $V_{DD}=50V, L=1.0mH$ )	$E_{AS}$	2592	mJ
Junction and Storage Temperature Range	$T_j, T_{stg}$	-55 to 175	
Thermal resistance, junction – ambient**	$R_{JA}$	46	/W
Thermal resistance, junction – case**	$R_{JC}$	0.6	

## Notes:

 \* Pulse width 300  $\mu s$ , duty cycle 2 %

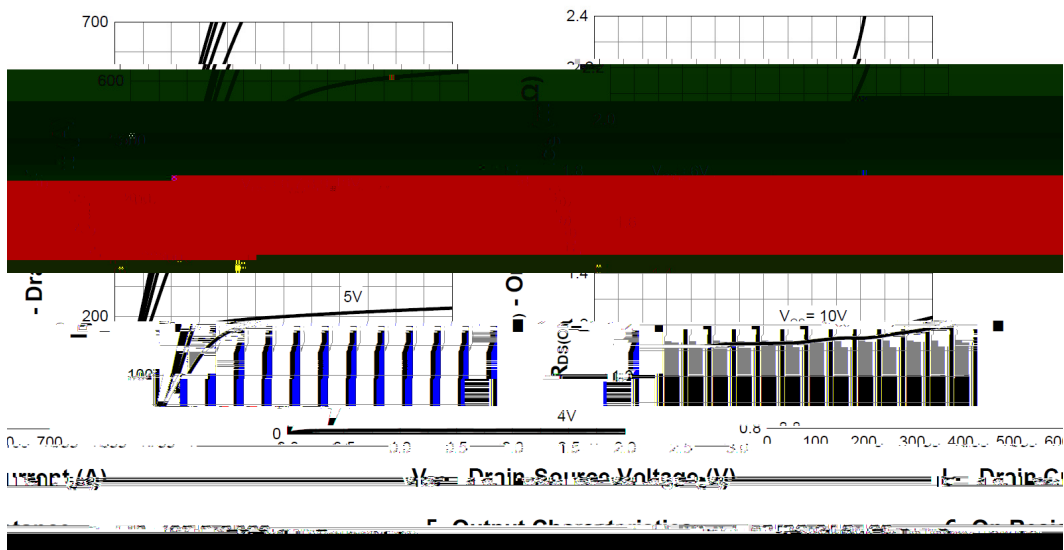
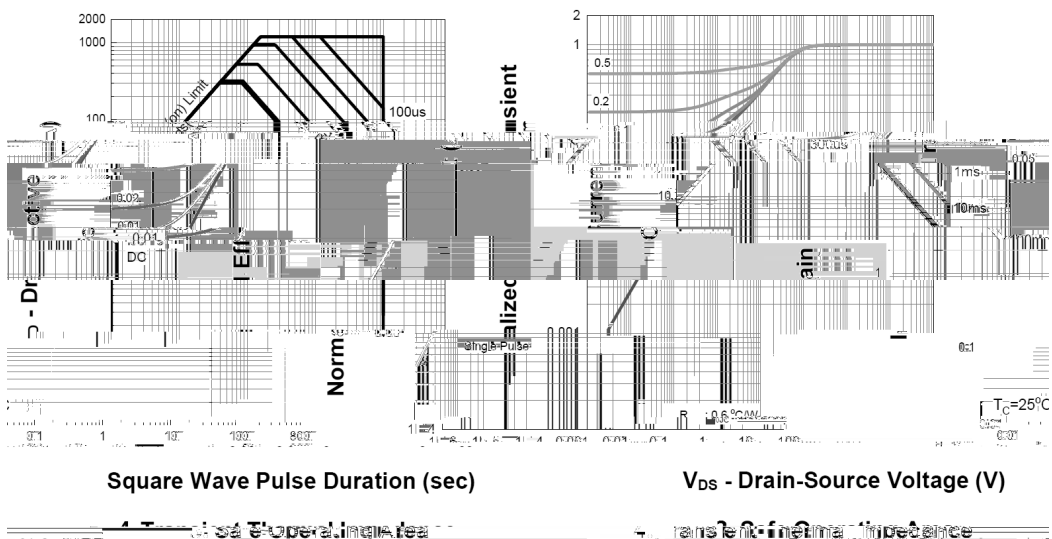
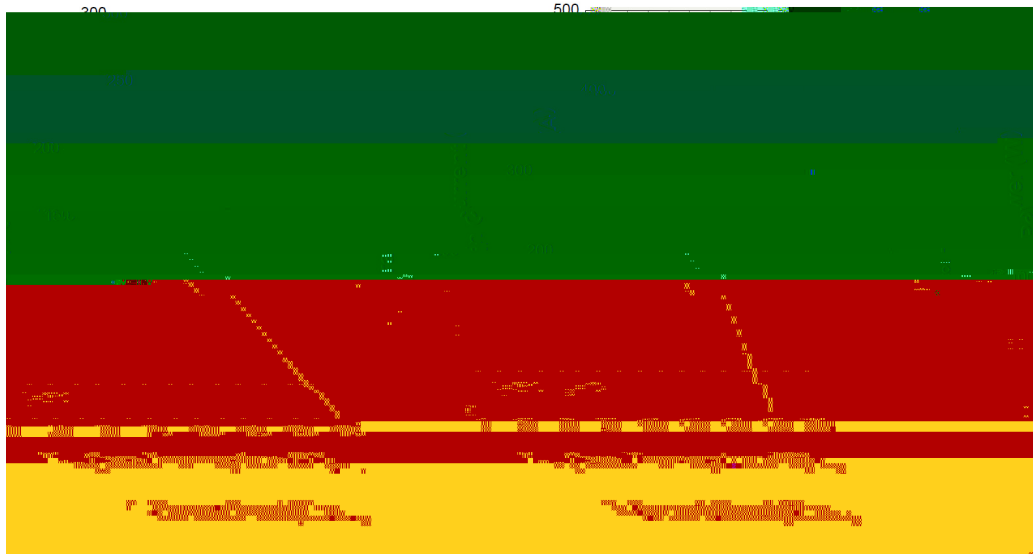
 \*\* Surface Mounted on 1 in<sup>2</sup> pad area, t 10sec

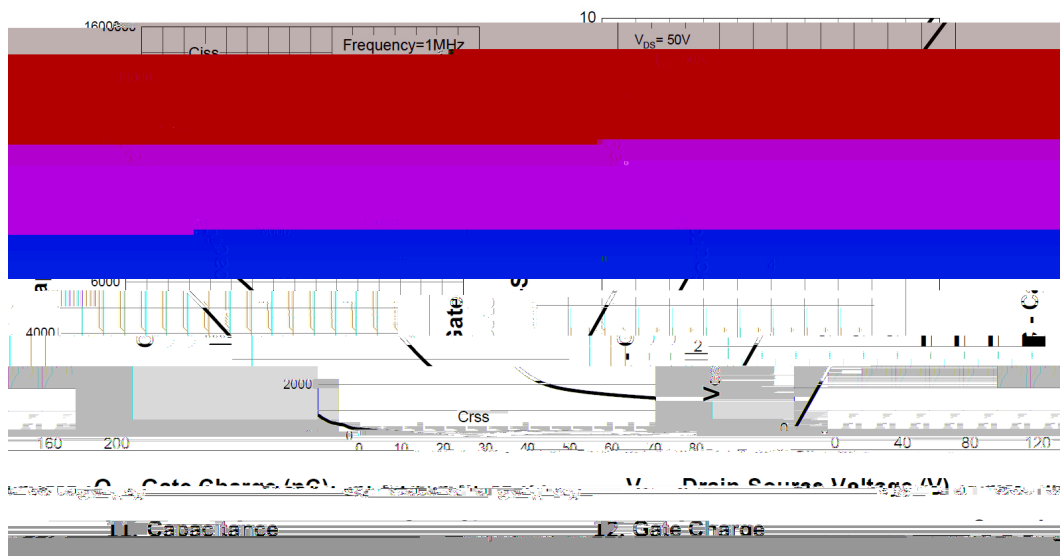
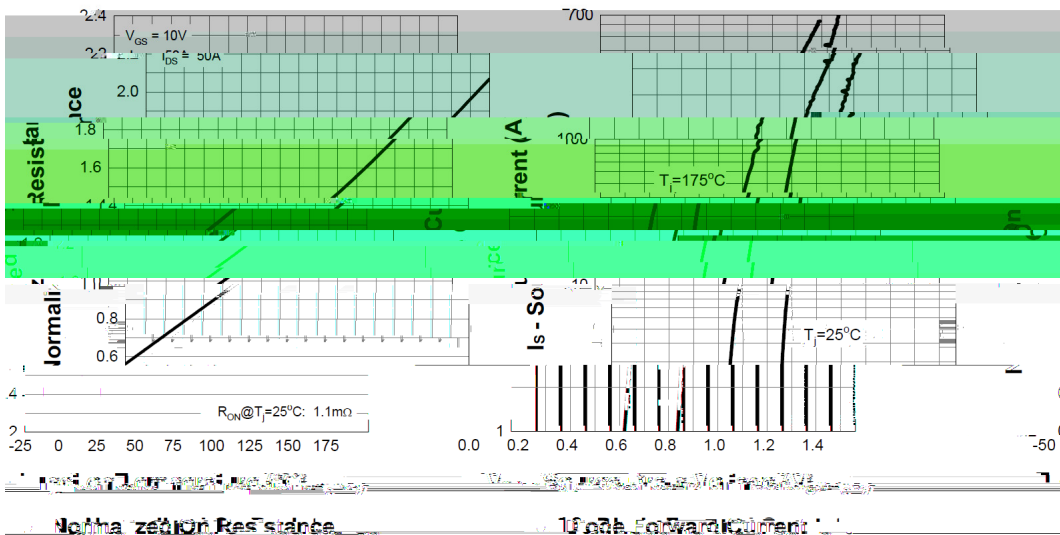
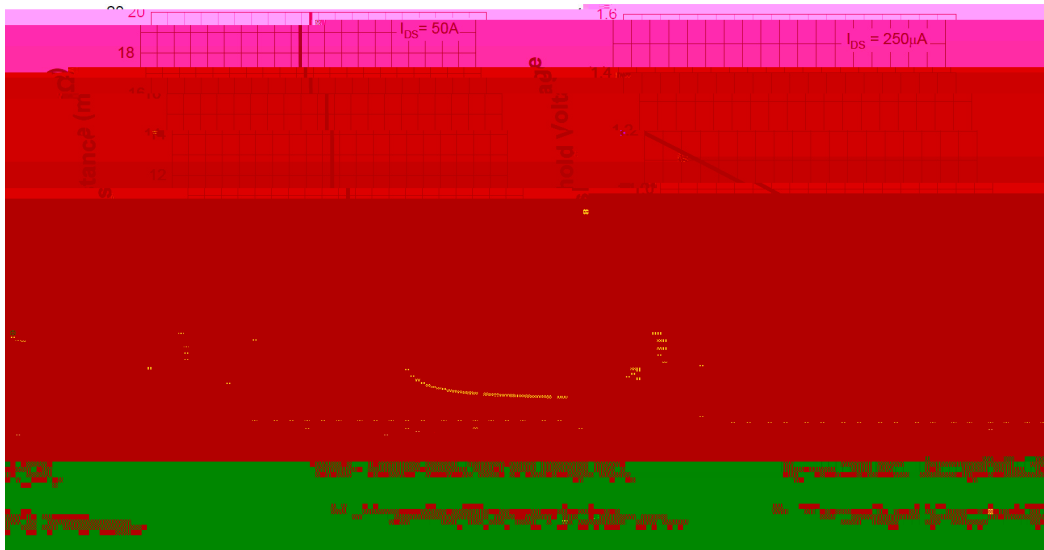
\*\*\* Limited by bonding wire

Parameter	Symbol	Test Conditions	Min
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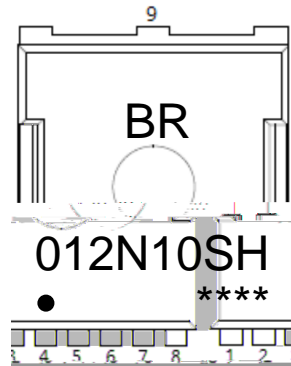
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Total Gate Charge	$Q_g$	$V_{GS}=10V, V_{DS}=50V$ $I_{DS}=50A$		196		nC
Gate Source Charge	$Q_{gs}$			68		
Gate Drain Charge	$Q_{gd}$			35		

Turn-On Delay Time  $t_{d80} 0.0 10.5 55.98 646.4003 T_m-.0008 T_c.0031 T_{wc} T_c15.0613 T_w[ ]1240 92(rn-On Del)-7 G S$









BR

012N10SH

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Note

BR                      Company Code

012N10SH          Product Type Code

\*\*\*\*:                      Lot No. Code, code change with Lot No.

## Temperature Profile for IR Reflow Soldering(Pb-Free)