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N-Channel Enhancement Mode Field Effect Transistor in a DFN1006-3L Plastic Package.

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Sensitive gate trigger current and Low Holding current.ESD protected up to 2KV,HF Product.

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Pin1:G Pin2:S Pin3:D

@ f Parameter		... Z Symbol	$f >$ Rating	% y Unit
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	18	V
Drain Current – Continuous		I_D	0.75	A
Pulsed Drain Current		I_{DM}	1.8	A
Power Dissipation		P_D	0.9	W
Storage Temperature Range		T_{stg}	-55 to 150	-
Thermal resistance, junction - ambient	t 0 10s	R_{JA}	100	- /W
	Steady-State		140	

Electrical Characteristics(Ta=25℃ ;)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Total Gate Charge	Q _g	V _{GS} =4.5V, V _{DS} =10V, I _D =0.4A		0.85		nC
Gate Source Charge	Q _{gs}			0.1		
Gate Drain Charge	Q _{gd}			0.25		
Turn-On Delay Time	t _{d(on)}	V _{GS} =4.5V V _{DS} =10V R _L =25 R _{GEN} =3		2		ns
Turn-On Rise Time	t _r			4		ns
Turn-Off Delay Time	t _{d(off)}			18		ns
Turn-Off Fall Time	t _f			8		ns

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Ô ? d • Ž ¢ / Electrical Characteristic Curve

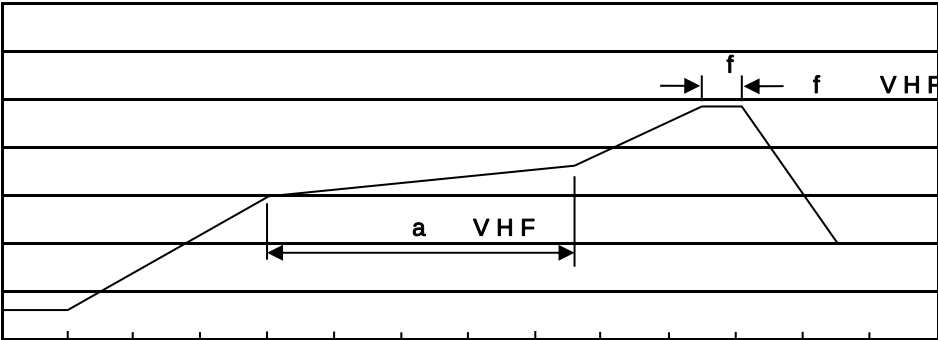
Ø □ =) ¢ / Package Dimensions

, M y f / Marking Instructions

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ŠWD t...•Žč (x/) / :KSVKXGZ[XK 6XULORK LUX /8 8KLRU] 9URJKXOTM 6

7HPSHUDWXUH



7LPH VHF

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- 10• Ä ½ “ † 150 ½180 - k ž • 60 ½90sec;
- 20• Q > “ † 245 r5 - k ž • 4 Ò 5 r0.5sec;
- 30•D N ò i Ò 0 , † 2 ½10 - /sec.

Note:

- 1.Preheating:150~180 - , Time:60~90sec.
- 2.Peak Temp.:245 r5 - , Duration:5 r0.5sec.
- 3. Cooling Speed: 2~10 - /sec.

ÂD /Cã p ¯ »] / Resistance to Soldering Heat Test Conditions

“ † y 260 r5 - ž • y 10 r1 sec. Temp.:260±5 Time:10±1 sec

G P á / Packaging SPEC.

2 & x / REEL

Package Type 7>û ~ E	Units ;>û H					Dimension ;>û p . (unit Åmm³)		
	Units/Reel /-	Reels/Inner Box -- /-	Units/Inner Box /-	Inner Boxes/Outer Box - /1ç	Units/Outer Box /1ç	Reel	Inner Box	Outer Boxç
DFN1006-3L	10,000	10	100,000	6	600,000	7 s ×8	180×120×180	390×385×205

„Đ y f / Notices