

# DTA114EKA

Rev.H Nov.-2024

## 5 é / Descriptions

SOT-23 PNP Silicon PNP Digital Transistor in a SOT-23 Plastic Package.

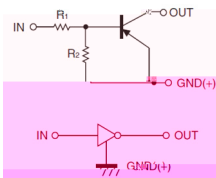
## α a / Features

With built-in bias resistors, simplify circuit design, reduce a quantity of parts and manufacturing process, HF Product.

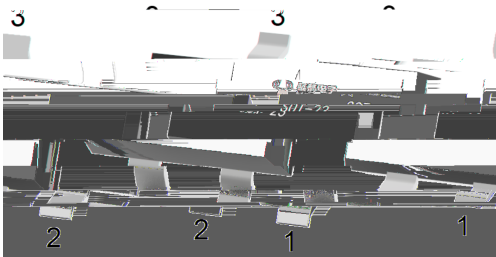
## Đ ÷ / Applications

Switching applications, interface circuit and driver circuit applications.

## Ã W] Ô . / Equivalent Circuit



## • Ô - æ / Pinning



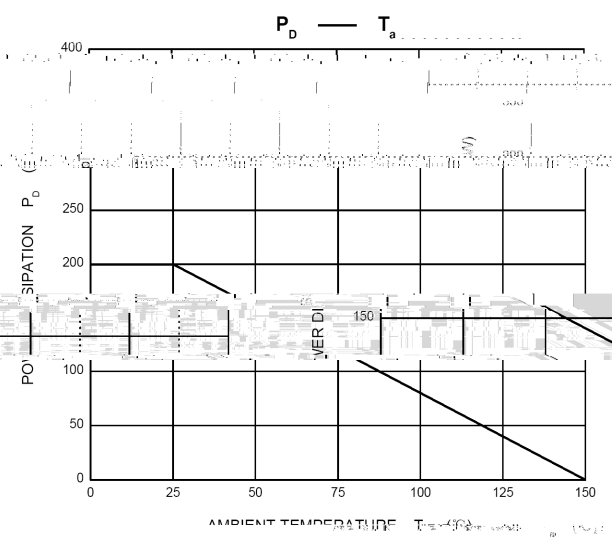
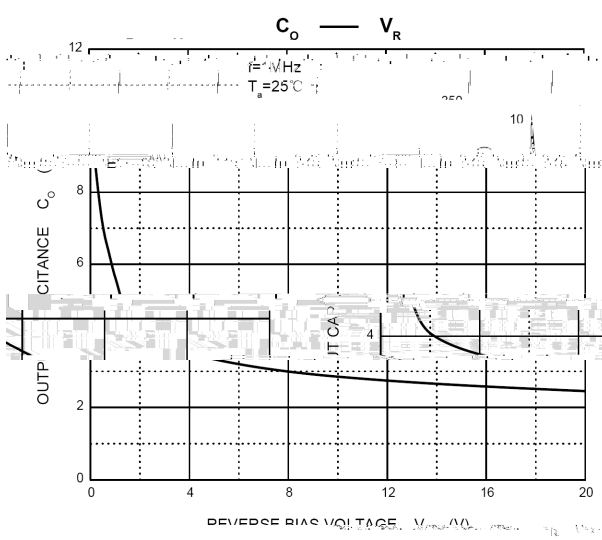
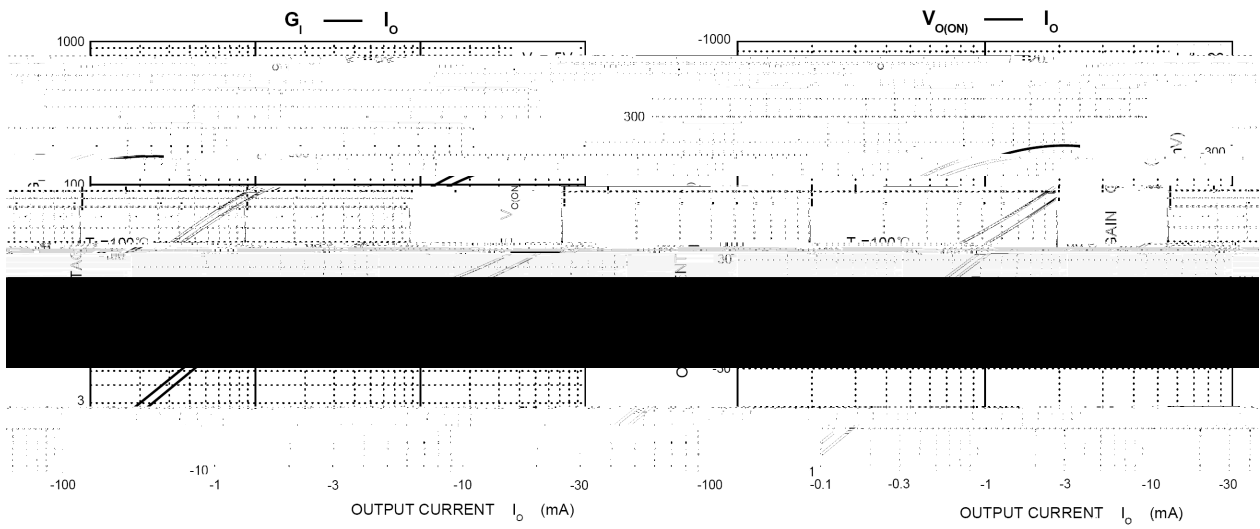
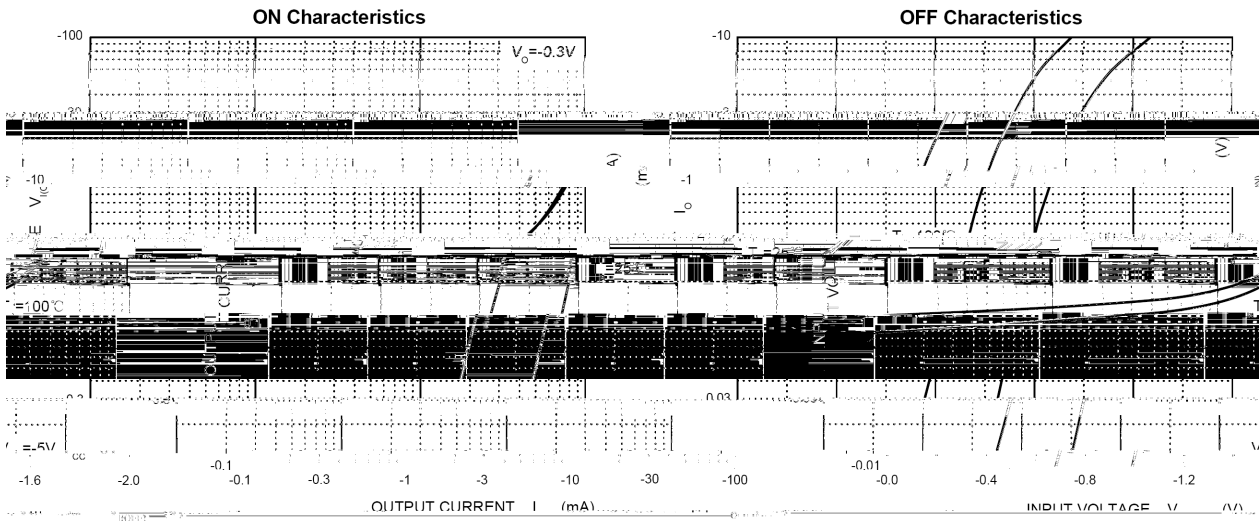
PIN1 IN PIN 2 GND PIN 3 OUT

## Z Ü B , M V / hFE Classifications & Marking

Marking	H14
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Parameter	Symbol	Rating	Unit
Output Voltage	$V_{CC}$	-50	V
Input Voltage	$V_{IN}$	10	V
		-40	V
Output Current	$I_O$	-50	mA
Peak Collector Current	$I_{CM}$	-100	mA
Power Dissipation	$P_D$	200	mW
Junction Temperature	$T_j$	-55 150	

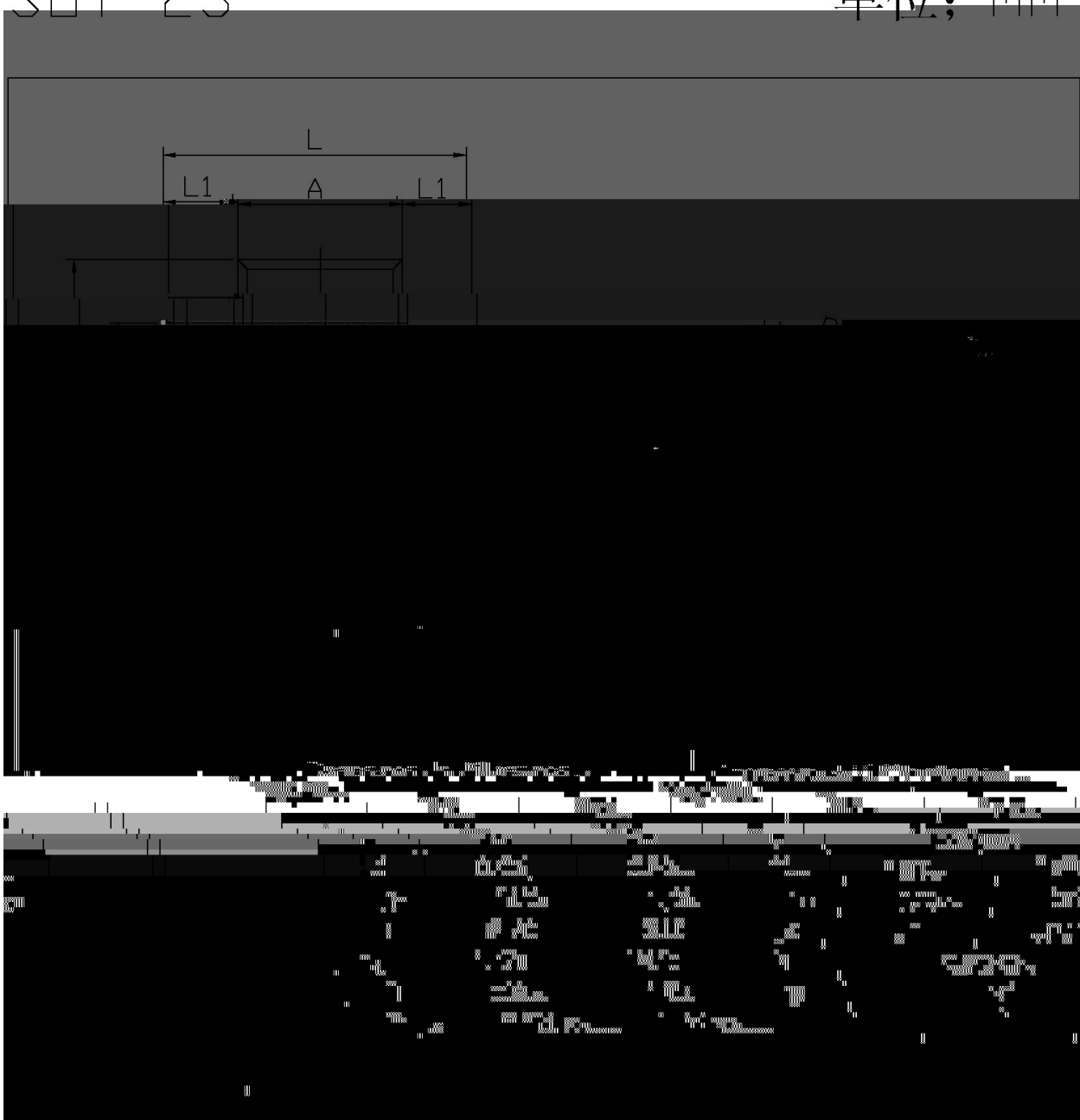
Electrical Characteristic Curve



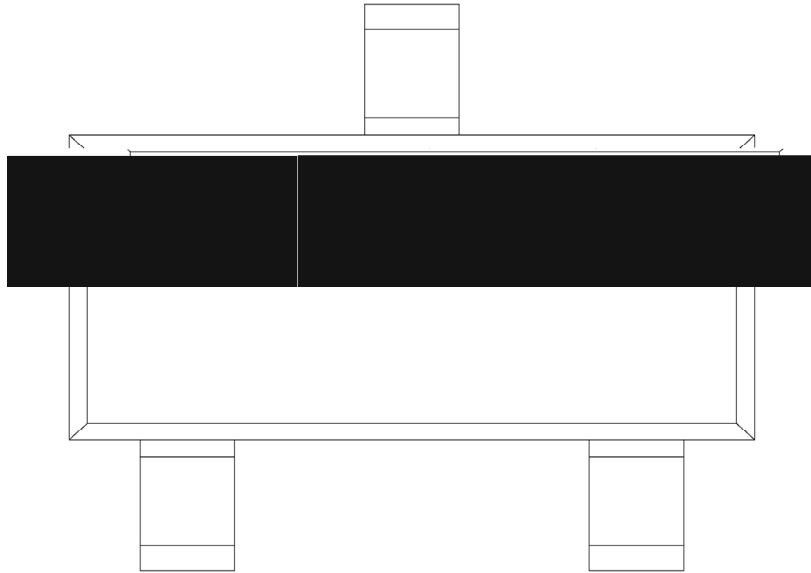
∅ □ = ) ∅ / Package Dimensions

SOT-23

单位: mm

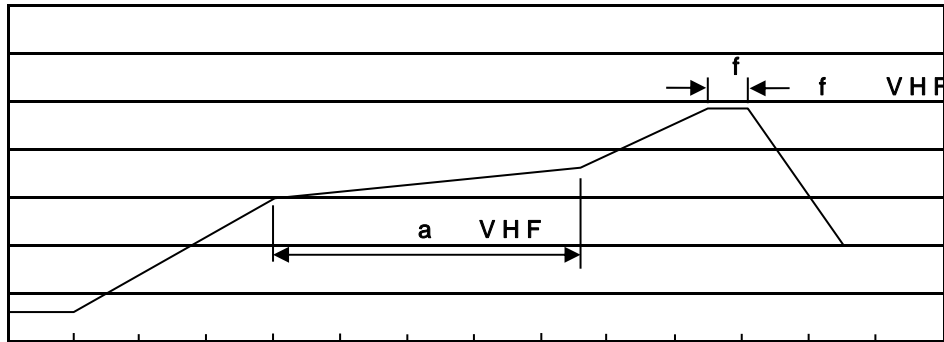


, M y f / Marking Instructions



šWD t...•Žç (x/ ) / :KSVKXGZ[XK 6XULORK LUX /8 8KLRU] 9URJKXOTM 6

7HPSHUDWXUH



7LPH VHF

Note:

- |   |         |           |   |
|---|---------|-----------|---|
| 1 | 150 180 | 60 90sec; | 1.Preheating:150~180 , Time:60~90sec.   |
| 2 | 245 5   | 5 0.5sec; | 2.Peak Temp.:245 5 , Duration:5 0.5sec. |
| 3 | 2 10    | /sec.     | 3. Cooling Speed: 2~10 /sec.            |

**Resistance to Soldering Heat Test Conditions**

260 5 10 1 sec. Temp.:260±5 Time:10±1 sec

**Packaging SPEC.**

/ REEL

Package Type 7>û ~ E	Units ;>û :H					Dimension ;>û p . (unit /Æmm <sup>3</sup> )		
	Units/Reel /--	Reels/Inner Box -- /-	Units/Inner Box /-	Inner Boxes/Outer Box - /1ç	Units/Outer Box /1ç	Reel	Inner Box	Outer Boxç
SOT-23	3,000	10	30,000	6	180,000	7 T×8	180×120×180	390×385×205

**Notices**