

/ Descriptions

Silicon PNP transistor in a TO-220 Plastic Package.

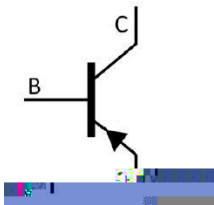
/ Features

Complement to KSD288.

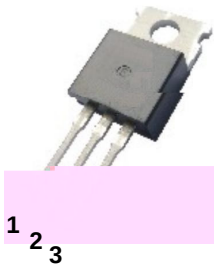
/ Applications

Low frequency power amplifier.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

h_{FE} Classifications Symbol	R	O	Y
h_{FE} Range	40 80	70 140	120 240

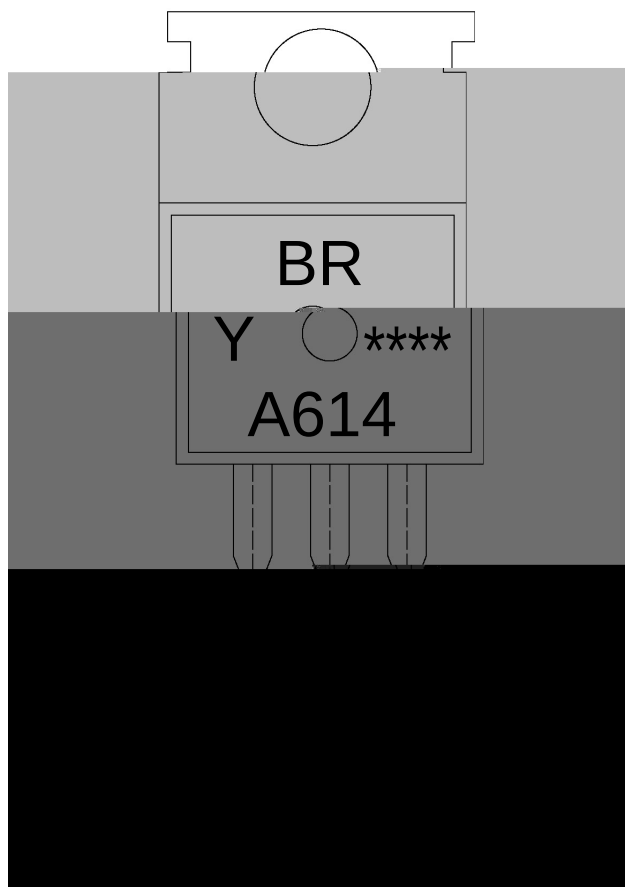
Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-80	V
Collector to Emitter Voltage	V_{CEO}	-55	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-3.0	A
Collector Power Dissipation	P_C	2.0	W
	$P_C(T_c=25^\circ C)$	25	W
Junction Temperature	T_J	150	
Storage Temperature Range	T_{stg}	-55 150	

Parameter Symbol

/ Package Dimensions



/ Marking Instructions



Note:

BR: Company Code

A614: Product Type.

Y: h_{FE} Classifications Symbol

****: Lot No. Code, code change with Lot No.

Figure 1 is a line graph showing the temperature profile of the sample during the experiment. The y-axis represents Temperature (°C) from 0 to 300, and the x-axis represents Time (sec) from 0 to 220. The profile shows a heating phase from 20°C to 255±5°C at 10°C/min, a dwell phase at 255±5°C for 5±0.5 sec, and a cooling phase. A zoomed-in view of the heating phase shows a linear increase from 20°C at 20 sec to 100°C at 100 sec, with a dwell at 100°C from 86 to 100 sec.

1	25	150	60	90sec;
2	255	5	5	0.5sec;
3		2	10	/sec.

[illegible]