

Silicon PNP transistor in a TO-126 Plastic Package.

Complement to TIP122L.

Medium power linear switching applications.



PIN1 Emitter PIN 2 Collector PIN 3 Base

/ **h_{FE} Classifications & Marking**

See Marking Instructions.

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-100	V
Collector to Emitter Voltage	V_{CEO}	-100	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-5.0	A
Collector Power Dissipation	$P_C(T_c=25)$	40	W
Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	

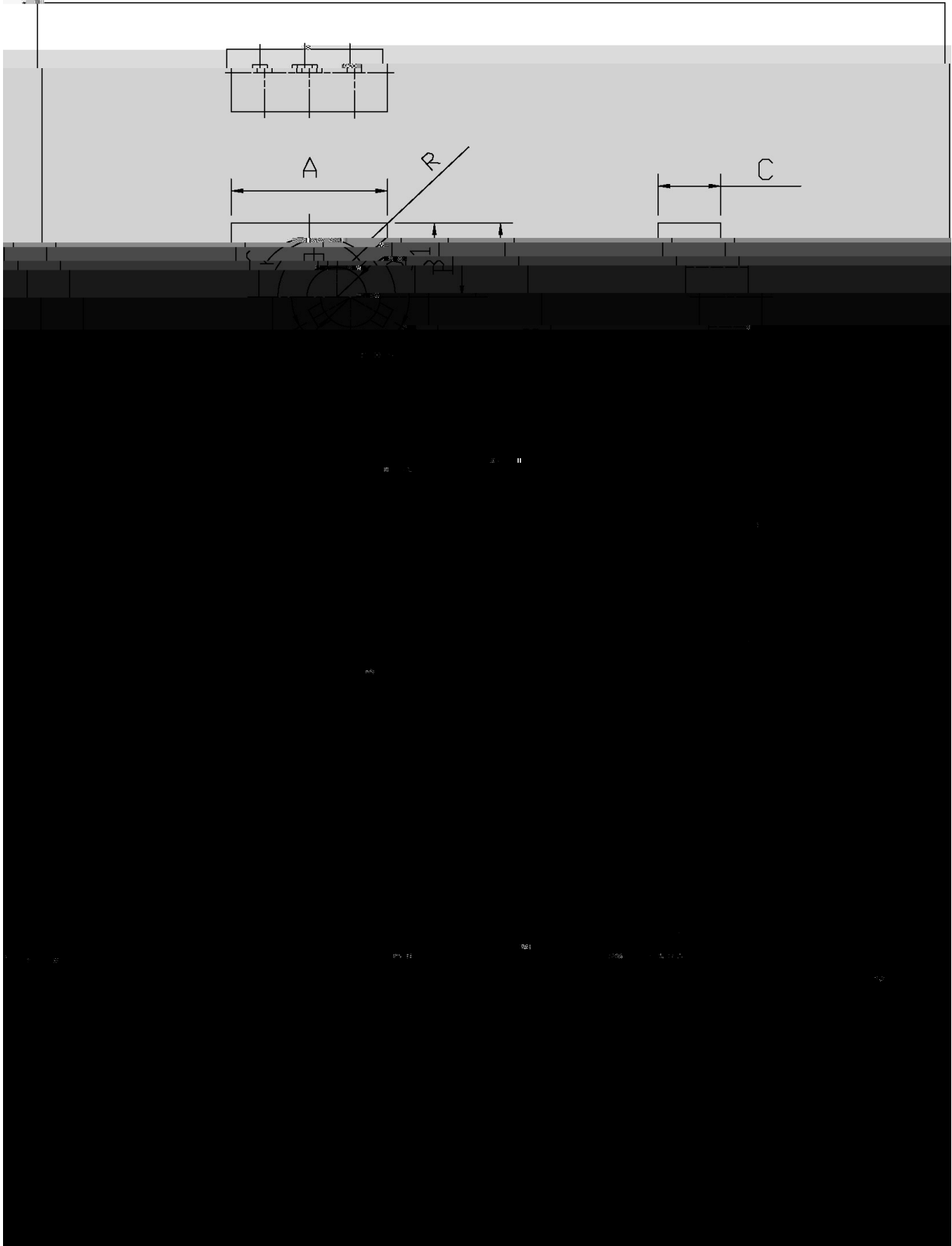
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector to Emitter Breakdown Voltage	V_{CEO}	$I_C=-100mA$ $I_B=0$	-100			V
Collector Cut-Off Current	I_{CEO}	$V_{CE}=-50V$ $I_B=0$			-2.0	mA
Collector Cut-Off Current	I_{CBO}	$V_{CB}=-100V$ $I_E=0$			-1.0	mA
Emitter Cut-Off Current	I_{EBO}	$V_{EB}=-5.0V$ $I_C=0$			-2.0	mA
DC Current Gain	$h_{FE(1)}$	$V_{CE}=-3.0V$ $I_C=-0.5A$	1000			
	$h_{FE(2)}$	$V_{CE}=-3.0V$ $I_C=-3.0A$	1000			
Collector to Emitter Saturation Voltage	$V_{CE(sat)(1)}$	$I_C=-3.0A$ $I_B=-12mA$			-2.0	V

TIP127L
Rev.F Mar.-2016

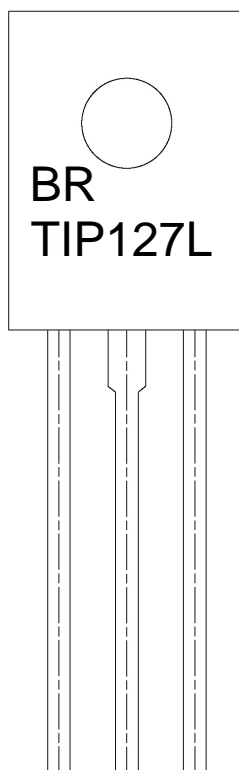
/ Package Dimensions

T0-126

单位: mm



/ Marking Instructions



Note:

BR: Company Code

TIP127L: Product Type.

***: Lot No. Code, code change with Lot No.

(